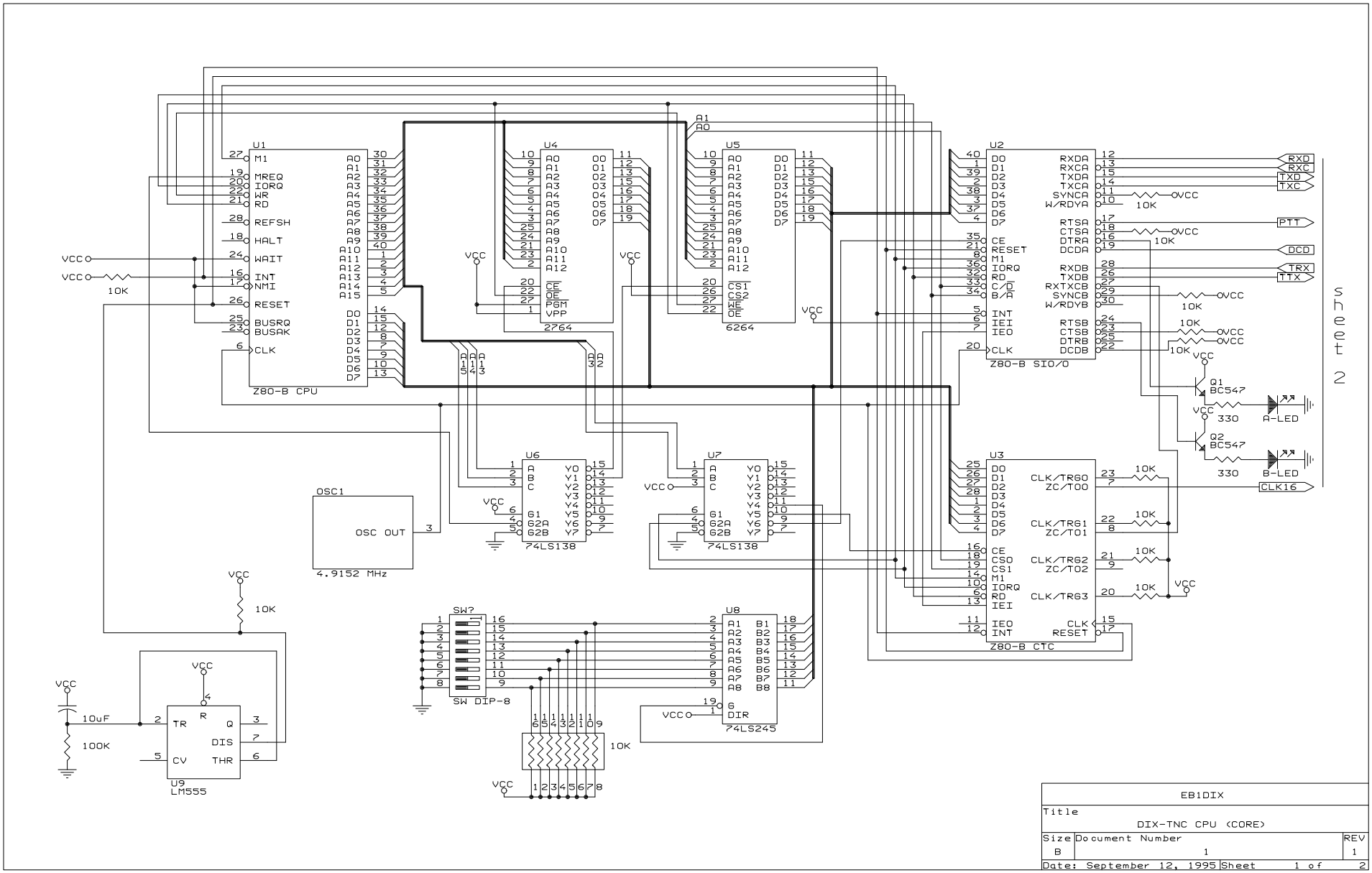
U1
Z80-B CPU
M1
MREQ
IORQ
WR
RD
REFSH
HALT
WAIT
INT
NMI
RESET
BUSRQ
BUSAK
CLK
A0 A1 A2 A3 A4 A5 A6 A7 A8 A9 A10 A11 A12 A13 A14 A15
D0 D1 D2 D3 D4 D5 D6 D7
U4
2764
CE
OE
PGM
VPP
U5
6264
CS1
CS2
WE
OE
U2
Z80-B SIO/O
RXDA
RXCA
TXDA
TXCA
SYNCA
W/RDYA
RTSA
CTSA
DTRA
DCDA
RXDB
TXDB
RXTXCB
SYNCB
W/RDYB
RTSB
CTSB
DTRB
DCDB
CE
RESET
M1
IORQ
RD
C/D
B/A
INT
IEI
IEO
CLK
RXD
RXC
TXD
TXC
PTT
DCD
TRX
TTX
CLK16
U3
Z80-B CTC
CLK/TRG0
ZC/TO0
CLK/TRG1
ZC/TO1
CLK/TRG2
ZC/TO2
CLK/TRG3
CE
CSO
CS1
M1
IORQ
RD
IEI
IEO
INT
CLK
RESET
Q1
BC547
Q2
BC547
A-LED
B-LED
330
U6
74LS138
U7
74LS138
A
B
C
G1
G2A
G2B
Y0 Y1 Y2 Y3 Y4 Y5 Y6 Y7
OSC1
OSC OUT
4.9152 MHz
SW?
SW DIP-8
U8
74LS245
A1 A2 A3 A4 A5 A6 A7 A8
B1 B2 B3 B4 B5 B6 B7 B8
G
DIR
U9
LM555
TR
R
Q
DIS
CV
THR
VCC
10K
100K
10uF
VCC
VCC
VCC