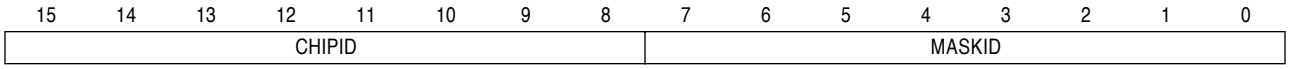
ID REGISTER

Inside the MC68328 and MC68EZ328, there is a specific **read only** register that can be used for chip identification purpose.

CHIP AND MASK ID REGISTER

The special **read only** register named Chip and Mask ID Register ($FFFFF004) is designed to support the identity of different products and different mask sets in the DragonBall family. The Chip and Mask ID register is a 16-bit register as shown below:



Address: $(FF)FFF004

ID Register

CHIPID - Identification of the chip in the DragonBall family. These members are MC68328 DragonBall and MC68EZ328 DragonBall EZ. This register allows for the addition of future DragonBall family members.

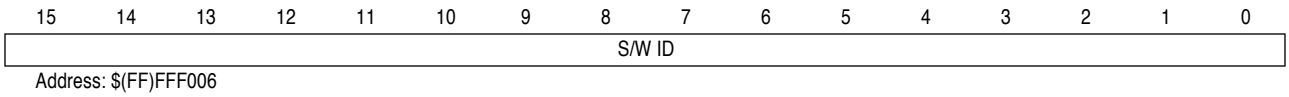
MASKID - Identification of a specific maskset for the identified chip. Masksets vary according to fab and revision. Any changes made to chip maskets, including metal fixes, will cause a revision to the MASKID.

Table 1:

		$FFF004	$FFF005	$FFF006	$FFF007
Chip	**Maskset**	**CHIPID**	**MASKID**	**S/W ID**	
MC68328	3G58E/ 0H51K	$1C (sysreg's default at reset)	N/A	N/A	N/A
	1H58B/ 0H38T	"3" ASCII - $33	"0" ASCII - $30	N/A	N/A
MC68EZ328	0H31J	$00	$01	$00	$00
	0J75C	"E" ASCII - $45	$02	$00	$00
	1J83G	"E" ASCII - $45	$05	$00	$00

SOFTWARE ID REGISTER

The MC68EZ328 DragonBall EZ also supports a register that is designed for application software use. In particular, this register can be used as a soft-key for specific application software running only when a preset value is recognized. This register is not for general use and is only valid for masksets that are generated for specific customer use. See Table 1 for default preset values.



S/W ID Register

S/W ID - The preset value for specific application software recognition. It can be hardcoded in the silicon maskset with a different value for different customers.